

IEEE EPS Technical Lectures by Dr. SF Wong

10th and 23rd May 2024



Invited Industrial Lecture:
Recent Advances in Semiconductor Packaging Technology

Date: 10 May 2024 (Friday)
Time: 10.00 am - 12.00 pm
Venue: The Cube, Level 2, Engineering Tower, Faculty of Engineering, Universiti Malaya.



JOIN US

Ir. Dr. Wong Shaw Fong
Engineering Director, Intel Malaysia
Elite@UM Fellow



ADVANCEMENT OF PACKAGING FOR GLOBAL SEMICONDUCTOR LANDSCAPE

23rd MAY 2024
11am-12pm



SPEAKER:
Dr Shaw Fong Wong
(Intel Technology Sdn Bhd)

Platform : MS Team
Link: <https://bit.ly/44MrJdt>

JAWATANKUASA HUBUNGAN LUAR DAN INDUSTRI (JKHLI) FKEE 2024



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Intel Technology Sdn Bhd
GPOC
UM TV

- As part of the ELITE@UM Fellow program, IEEE EPS Committee - Shaw Fong Wong from Intel Foundry (Intel Technology Malaysia) delivered an industry lecture on “Recent Advancements in Electronic Packaging” at the prestigious UM lecture hall.
- Similar range of topics also extended to UTHM as part of the online webinars.
- Both sessions (with > 20 students each) concluded with a vibrant Q&A segment, fostering a dynamic exchange of knowledge and ideas among the participants and the esteemed speakers. This technical lecture also jointly supported by IEEE Malaysia Section (EPS Chapter) as well as IEM (eETD & MaTD) teams.

